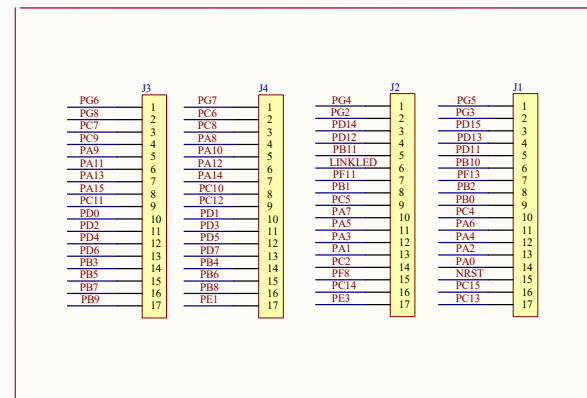




ISPT时：短接P3的1、2脚，短接P4
运行时：断开P3的1、2脚，短接其2、3脚：P4不管理

P5

1 2 3

Header 3

3V3

R24

BOOT0

10K

GND

使用PB2引脚：断开P4

P6

1 2

Header 2

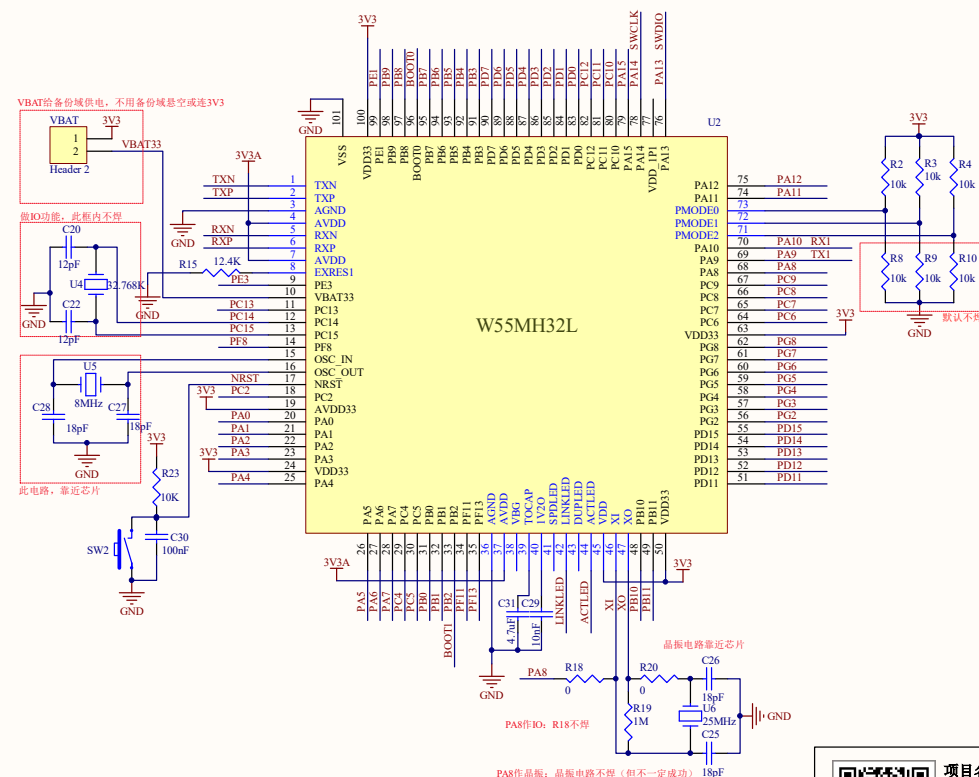
BOOT1

PB2

R24

10K

GND



The diagram shows two pin headers. The top header is labeled 'SWD' and has three pins: pin 1 is connected to SWCLK, pin 2 is connected to SWDIO, and pin 3 is connected to GND. The bottom header is labeled 'ISP' and also has three pins: pin 1 is connected to TX1, pin 2 is connected to RX1, and pin 3 is connected to GND.