

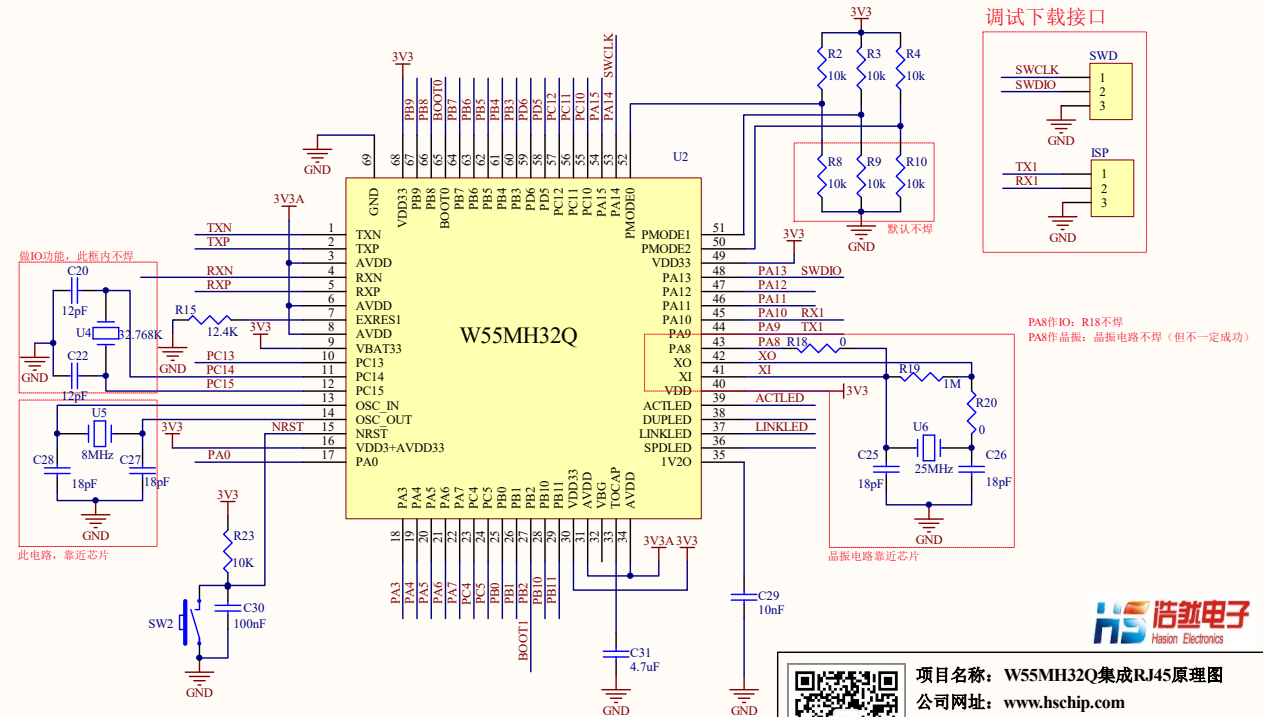
The diagram illustrates a power supply circuit with the following components and layout details:

- Input Section:** A 5V input is connected to header P2. A switch SW1 is used to select between the 5V input and a 3V3 input from header P1.
- Regulation Section:** The selected input passes through a 470R resistor (R1) to the IN pin of the voltage regulator U1 (SE8117T33). The regulator's GND pin is connected to ground, and its VOUT pin provides the 3V3 output.
- Output Section:** The 3V3 output is filtered by a 4.7uF/16V capacitor (C15) and a 100nF capacitor (C16). The output is then connected to a series of capacitors (C4-C14) and an inductor (L1) to provide a 3V3A output.
- LED Section:** An LED (LD1, LED-0603) is connected to the 3V3 output through a 470R resistor (R1) and a 1uF/16V capacitor (C17).
- Component Values:**
 - Resistors: R1 = 470R
 - Capacitors: C1 = 4.7uF, C2 = 100nF, C4-C14 = 100nF, C9-C14 = 100nF, C15 = 4.7uF/16V, C16 = 100nF, C17 = 1uF/16V
 - Inductor: L1 = 120R
- Layout Notes:**
 - Header P1 is labeled "Header 3" and has pins 1, 2, and 3.
 - Header P2 is labeled "Header 3" and has pins 1, 2, and 3.
 - The output section (C4-C14) is highlighted with a red box, indicating a critical area for layout.
 - The text "框内电容靠近相同标号的芯片引脚和标号" (Capacitors in the box should be placed close to the pins and labels of chips with the same label number) is present.

	J1		J2
PC13	1	PB9	1
PC14	2	PB8	2
PC15	3	PB7	3
NRST	4	PB6	4
PA0	5	PB5	5
PA3	6	PB4	6
PA4	7	PB3	7
PA5	8	PD6	8
PA6	9	PD5	9
PA7	10	PC12	10
PC4	11	PC11	11
PC5	12	PC10	12
PB0	13	PA15	13
PB1	14	PA14	14
PB2	15	PA13	15
PB10	16	PA12	16
PB11	17	PA11	17
LINKLED	18	PA10	18
PAS	19	PA9	19

ISP时：短接P3的1、2脚，短接P4运行时：断开P3的1、2脚，短接其2、3脚；P4不管

Diagram illustrating the connection of the ISP pin header (P3) to the 3V3 power supply and the BOOT0 pin. The header has pins 1, 2, and 3. Pin 1 is connected to 3V3. Pin 2 is connected to R25, which is connected to BOOT0. Pin 3 is connected to GND. A 10K resistor is connected between R25 and BOOT0.



晶振电路靠近芯片

PA8作IO: R18不焊
PA8作品振: 晶振电路不焊 (但不一定成功)



项目名称: W55MH32Q集成RJ45原理图
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