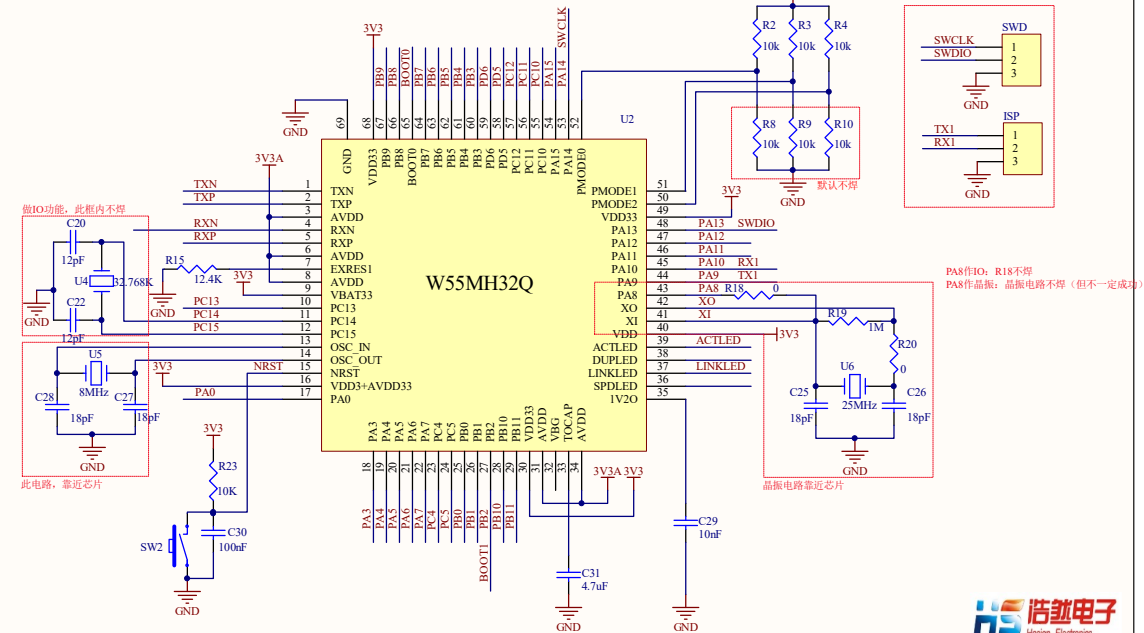


	J1		J2
PC13	1	PB9	1
PC14	2	PB8	2
PC15	3	PB7	3
NRST	4	PB6	4
PA0	5	PB5	5
PA3	6	PB4	6
PA4	7	PB3	7
PA5	8	PB6	8
PA6	9	PD5	9
PA7	10	PC12	10
PC4	11	PC11	11
PC5	12	PC10	12
PB0	13	PA15	13
PB1	14	PA14	14
PB2	15	PA13	15
PB10	16	PA12	16
PB11	17	PA11	17
LINKLED	18	PA10	18
PA8	19	PA9	19

ISP时：短接P3的1、2脚，短接P4
运行时：断开P3的1、2脚，短接其2、3脚；P4不管

The diagram illustrates the pin configurations for the P3 and P4 ports. For the P3 port, pin 1 is connected to a 3V3 supply, pin 2 is connected to a resistor R25, and pin 3 is connected to ground. The resistor R25 is connected to the BOOT0 pin. For the P4 port, pin 1 is connected to a resistor R24, and pin 2 is connected to ground. The resistor R24 is connected to the BOOT1 pin. The text indicates that during ISP, pins P3-1 and P3-2 are shorted, and during runtime, pins P3-1 and P3-2 are disconnected, and pins P3-2 and P3-3 are shorted. The P4 port is not connected during runtime.

[illegible]

项目名称: W55MH32Q变压器原理图
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