

The diagram illustrates a power supply circuit layout. Key components and their connections include:

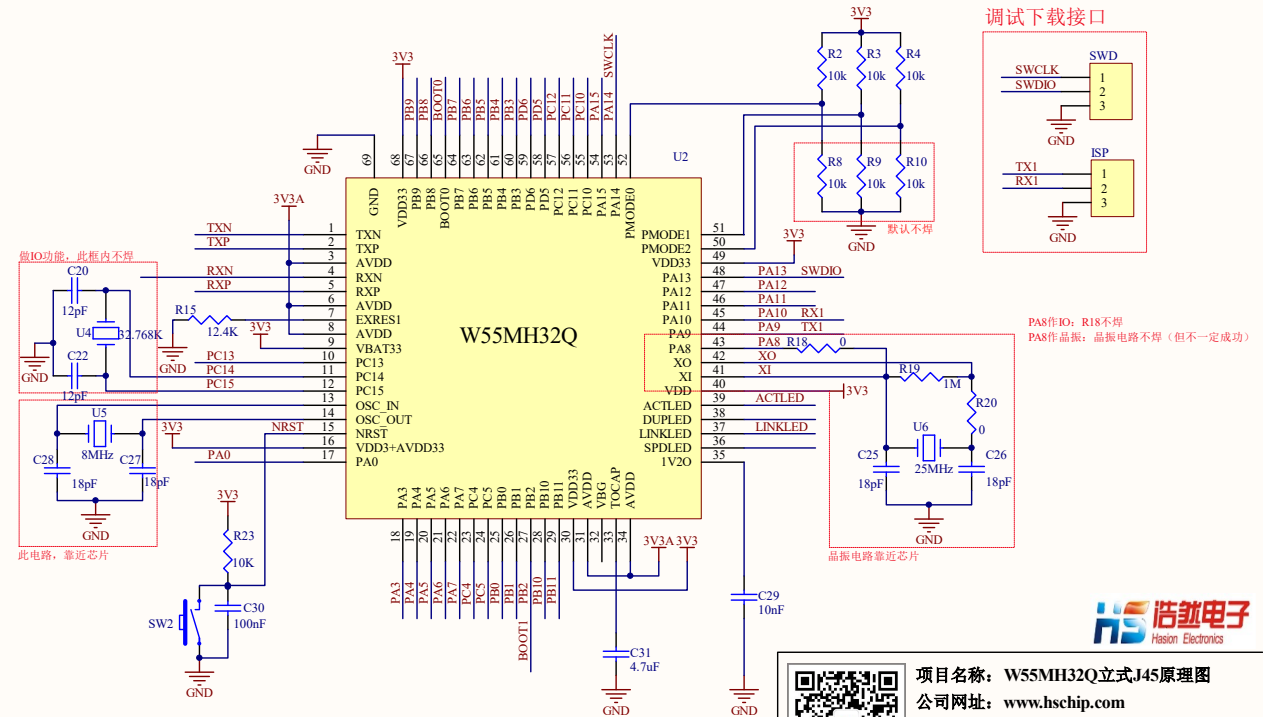
- Input Section:** A 5V input is connected to a header (P1) and a switch (SW1). A 3V3 input is connected to a header (P2).
- DC-DC Converter:** The converter (U1: SE8117T33) has its IN pin connected to the 5V input, VOUT pin connected to the 3V3 output, and VTAP pin connected to the 3V3A output.
- Output Filter:** The 3V3 output is filtered by a capacitor (C15: 4.7uF/16V). The 3V3A output is filtered by a capacitor (C16: 100nF).
- LED:** An LED (LD1: LED-0603) is connected to the 3V3 output through a resistor (R1: 470R).
- Capacitors:** Various capacitors (C1-C16) are used for filtering and decoupling. C1-C8 are 100nF capacitors, C9-C14 are 100nF capacitors, C15 is 4.7uF/16V, and C16 is 100nF.
- Layout Notes:** The layout includes a red box highlighting the output filter section, with a note: "框内电容靠近相同标号的芯片引脚和标号" (Capacitors in the box are placed near the corresponding chip pins and labels).

	J1		J2
PC13	1	PB9	1
PC14	2	PB8	2
PC15	3	PB7	3
NRST	4	PB6	4
PA0	5	PB5	5
PA3	6	PB4	6
PA4	7	PB3	7
PA5	8	PD6	8
PA6	9	PD5	9
PA7	10	PC12	10
PC4	11	PC11	11
PC5	12	PC10	12
PB0	13	PA15	13
PB1	14	PA14	14
PB2	15	PA13	15
PB10	16	PA12	16
PB11	17	PA11	17
LINKLED	18	PA10	18
PAS	19	PA9	19

ISP时：短接P3的1、2脚，短接P4运行时：断开P3的1、2脚，短接其2、3脚；P4不管

Diagram illustrating the connection for ISP programming:

- P3 Header:** Pins 1 and 2 are shorted. Pin 3 is connected to a 3V3 supply through a resistor R25.
- P4 Header:** Pins 1 and 2 are shorted. Pin 3 is connected to a 3V3 supply through a resistor R24.
- Both headers are connected to GND.

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